

Changes to the previous version are marked with a side bar |

This work instruction describes the requirements to be met by PCB manufacturers for panel design.

1. Specifications for Panel Design

The size of the panel and the arrangement of the individual patterns within the panel are defined by SIEB & MEYER AG via the Gerber File.

1.1. Panel Surfaces

The free panel surfaces on outer layers shall be flooded completely with copper. A grid with lines and points is allowed for inner layers.

1.2. Panel Edge

The panel edge shall remain free of copper in a width of 0.5 mm (see figure 1)

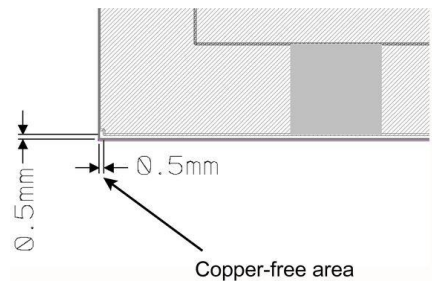


Figure 1 (copper-free edge)

1.3. Panel Corners

The panel corners shall be rounded with a radius of 1 mm (see figure 2).

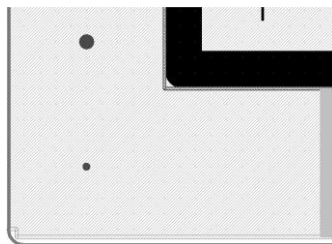


Figure 2 (rounded edge)

1.4. Panel Contour

The panel contour shall be routed.

1.5. Break-off Areas (If Routed)

The position and type of the **break-off areas** is defined by SIEB & MEYER AG. Figure 3 shows the interruption of the routing contour.

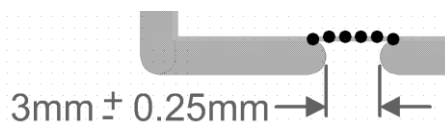


Figure 3 (break-off area)

2. Check Coupons for Quality Control

2.1. Measuring Surface for the Thickness of the Metal Layer

The measuring surface for the thickness of the metal layer shall be placed somewhere in the edge of the panel. Figure 4 shows the dimensions of the measuring surface.

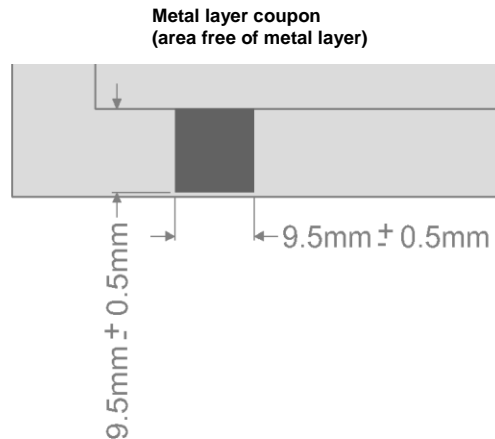


Figure 4 (measuring surface)

2.2. Check Surface for Layer Structure

The check surface for the layer structure shall be placed somewhere in the edge of the panel. For this, all layers must be guided outside the panel to allow final grinding.

2.3. PT Holes (Plated through holes = metalized holes)

All hole diameters of this type shall be done with a solder pad (circumferential, with a width of 0.3 mm) into the panel edge. This row of holes shall be placed with ascending or descending hole diameters somewhere in the edge of the panel.

2.4. NPT Holes (Non-plated through holes)

All hole diameters of this type shall be placed in the edge of the panel. This row of holes shall be placed with ascending or descending hole diameters somewhere in the edge of the panel.